

Annika 1.1 Block Diagram



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PCB STACK UP

6L

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1
LAYER 4 : IN2
LAYER 5 : VCC
LAYER 6 : BOT

10.1" LCD Panel
Page 16

CRT
Page 12

Intel Pineview-M
Micro-FCBGA8
22 x 22 mm
TDP~5.5W
Page 3~5

DDR3 667MT/s
Single Channel

DDR3 SO-DIMM
2GB Max.
Page 11

XDP
Page 31

CLOCK GEN
9LRS3165
Page 2

SATA

2.5"HDD/SSD
Page 19

Intel Tigerpoint
17 x 17 mm
MMAP 360 Balls
TDP~1.5W
Page 6~10

USB 2.0

0,1,2
USB2.0 Port x3
Page 21

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Card Reader
Page 13
Card Reader Socket
Page 13

5
Bluetooth/WLAN
Page 20

6
Touch Screen
Page 22

7
WWAN
Page 20
SIM Card Socket
Page 20

8
Webcam
Page 16

PCI-Express

X1
WWAN
Page 20
SIM Card
Page 20

X1
LAN
Realtek
RTL8103EL-VB
10/100
Page 15
RJ 45
Page 15

X1
WLAN
Page 20

X1
HD Decoder
Page 18
DDR II
64MByte
Page 18

SYSTEM POWER
+3VPCU/+5VPCU(RT8206)
PAGE 24

DDR 3 SMDDR_VTERM
+0.75VSMVREF/+1.5VSUS(RT8207)
PAGE 25

CPU CORE RT8152D
PAGE 26

SYSTEM CHARGER ISL6251AHAZ-T
PAGE 27

GFX CORE(RT9025)
+1.2V(RT9025)
+1.5V(RT9025)
PAGE 28
PAGE 29
PAGE 30

VCCP 1.05V(RT8209A)
PAGE 29

Touch Pad
Keyboard
Page 22

Power SW
Page 14

ENC KBC
KB3926D2
Page 23

AUDIO CODEC
92HD80BX
Page 17

BIOS
SPI Flash
Page 23

FAN
G991
Page 22

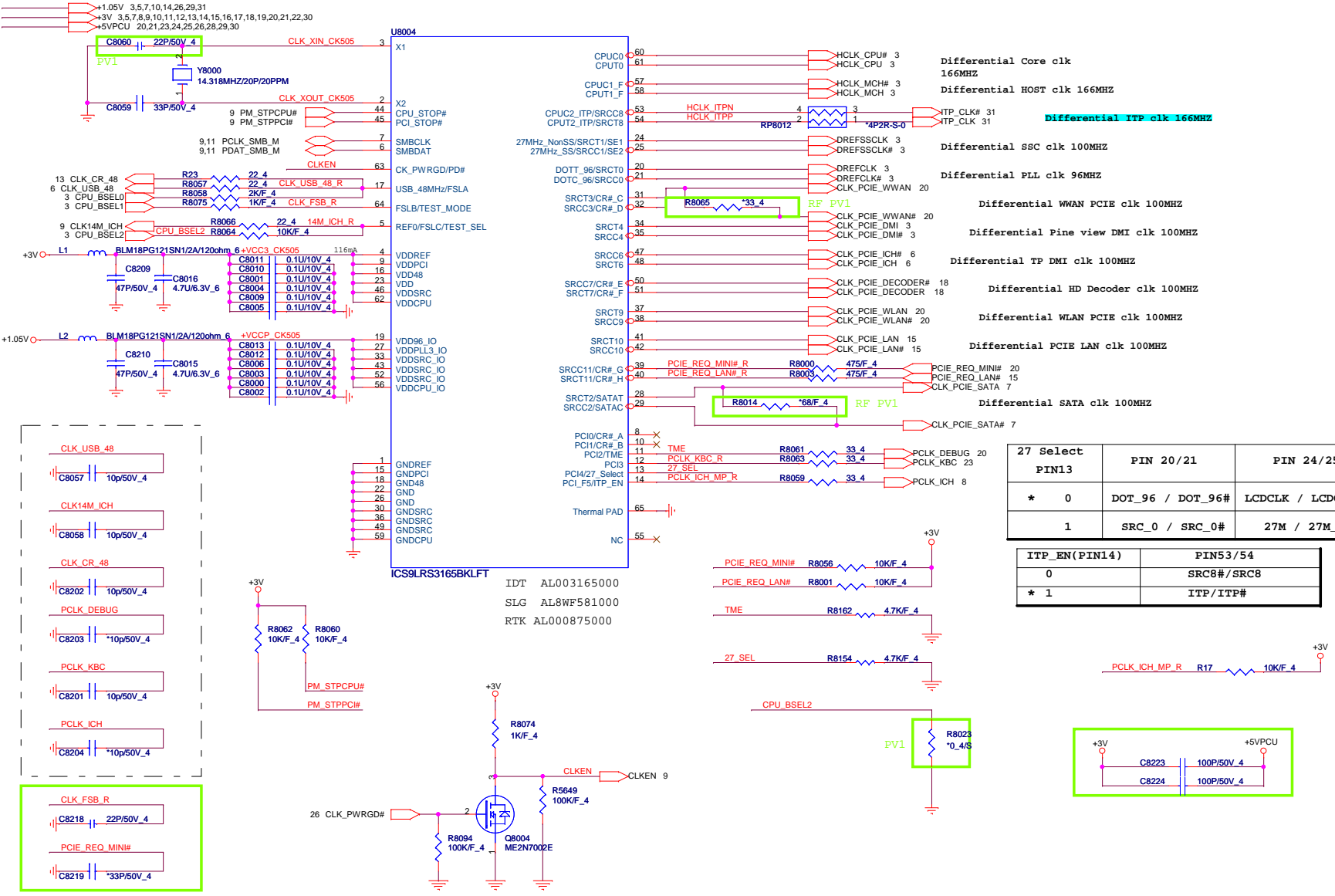
Int SPK
Page 17

Digital MIC
Page 17

HP/MIC
COMBO JACK
Page 17

Quanta Computer Inc. PROJECT : Annika		
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	BLOCK DIAGRAM	1A
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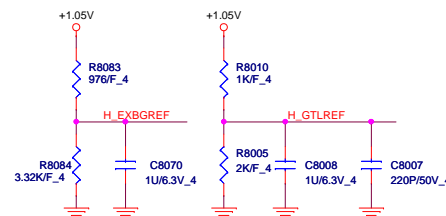
<http://rusefix.com>

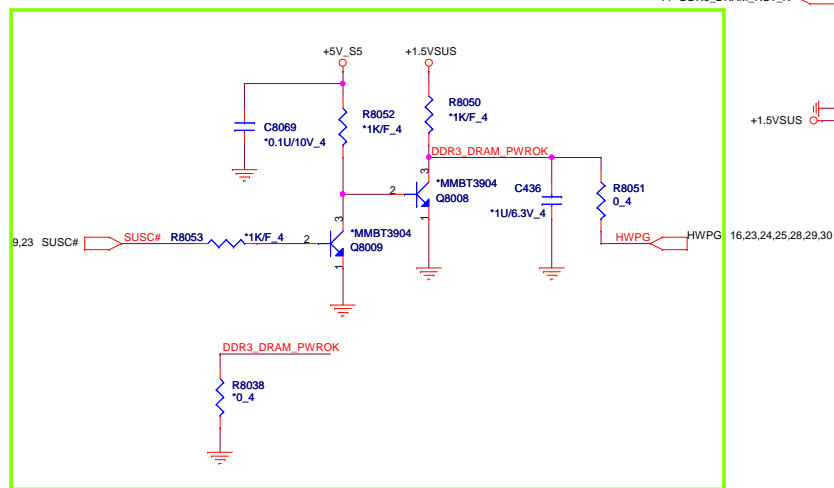
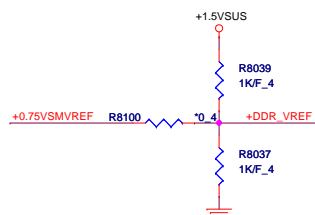


27 Select	PIN13	PIN 20/21	PIN 24/25
*	0	DOT_96 / DOT_96#	LCDCLK / LCDCLK#
	1	SRC_0 / SRC_0#	27M / 27M_SS

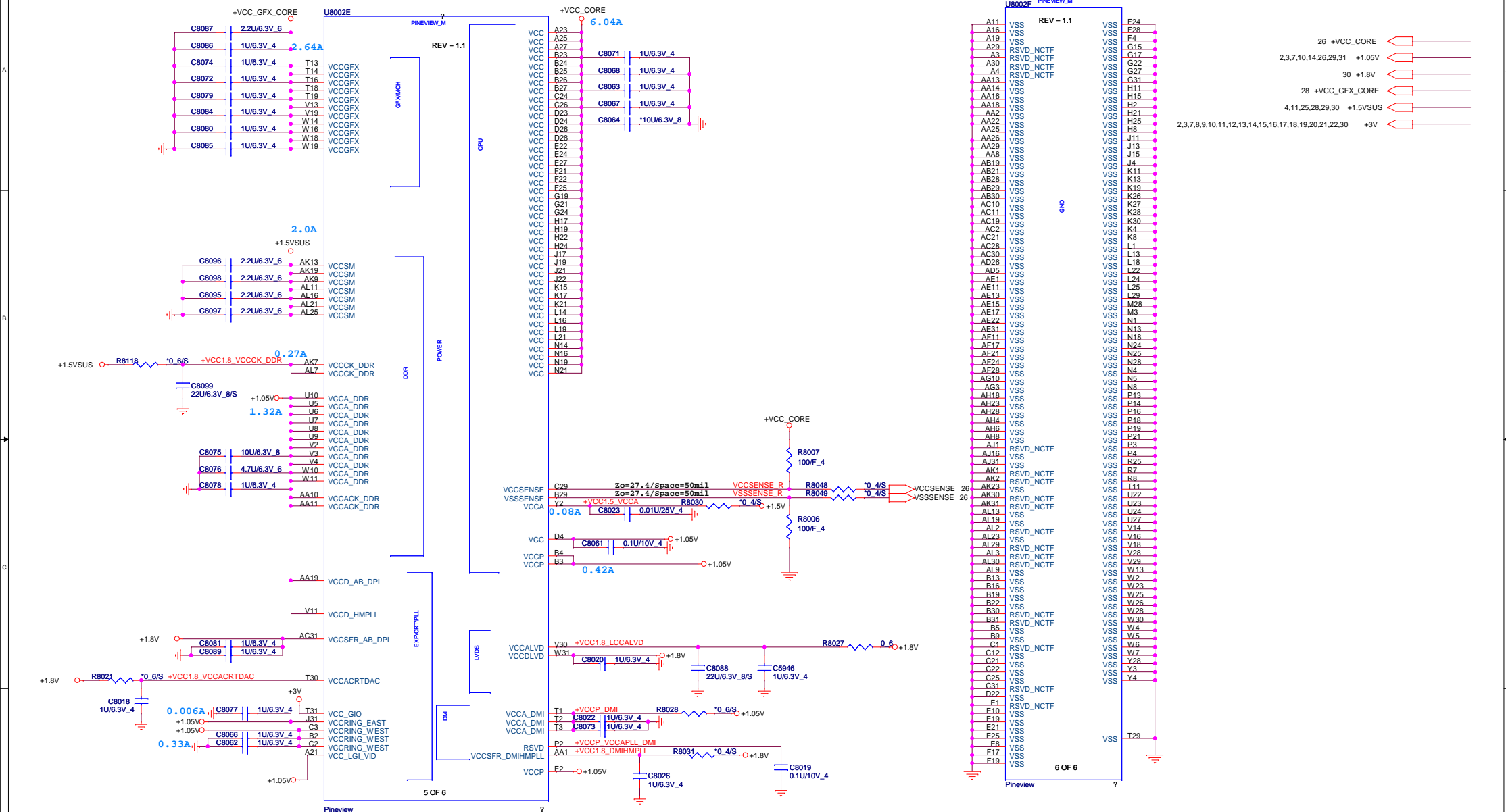
ITP_EN(PIN14)	PIN53/54
0	SRC8#/SRC8
* 1	ITP/ITP#

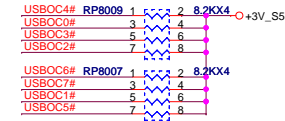
FSC	FSB	FSA	CPU	SRC	PCI	REF	USB	DOT	Spread
BSEL2	BSEL1	BSEL0							%
0	0	0	266.66	100	33.33	14.318	48	96	0.5 Down
0	0	1	133.33	100	33.33	14.318	48	96	0.5 Down
0	1	0	200.00	100	33.33	14.318	48	96	0.5 Down
0	1	1	166.66	100	33.33	14.318	48	96	0.5 Down
1	0	0	333.33	100	33.33	14.318	48	96	0.5 Down
1	0	1	100.00	100	33.33	14.318	48	96	0.5 Down
1	1	0	400.00	100	33.33	14.318	48	96	0.5 Down
1	1	1							
RESERVED									

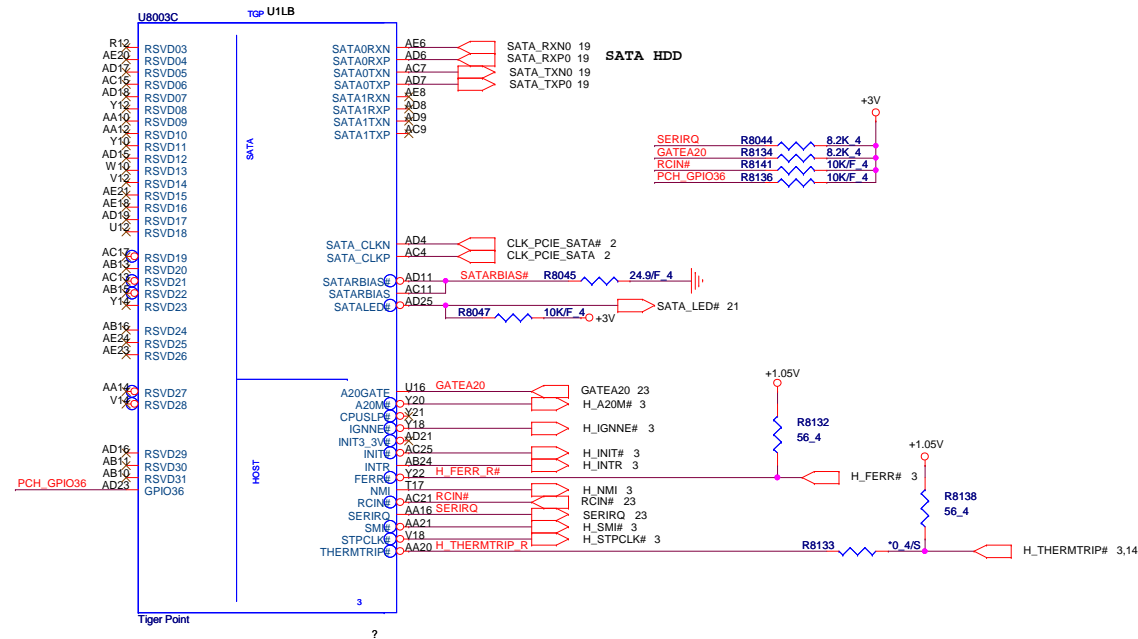




Intel CRB





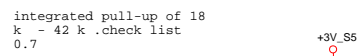




A16 SWAP Override strap

IRQ	Description
PIRQA	USB UHCI Controller #1, #4
PIRQB	AC'97 Codec; option for SMBUS
PIRQC	USB UH Controller #3; SATA/IDE Native Mode
PIRQD	USB UHCI Controller #2
PIRQE	Internal LAN; Option for SCI, TCO, HPET#0,1,2
PIRQF	Option for SCI, TCO, HPET#0,1,2
PIRQG	Option for SCI, TCO, HPET#0,1,2
PIRQH	USB EHCI Controller; Option for SCI, TCO, HPET#0,1,2

PCI_GNT#2	Internal PU Should not be PD
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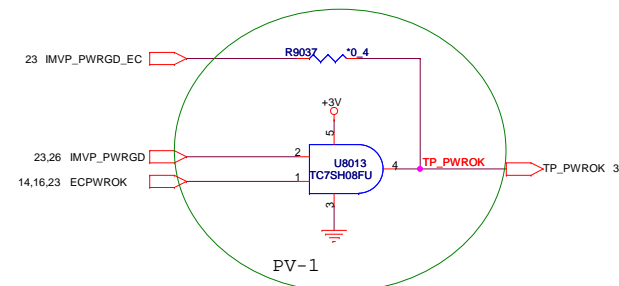


PLTRSTB# R8029 100K/F 4
LAN_RST# R8165 8.2K 4
TP_PWROK R8211 10K/F 4
RSMRST# R8212 10K/F 4

10-k pull-down to GND check list 0.7

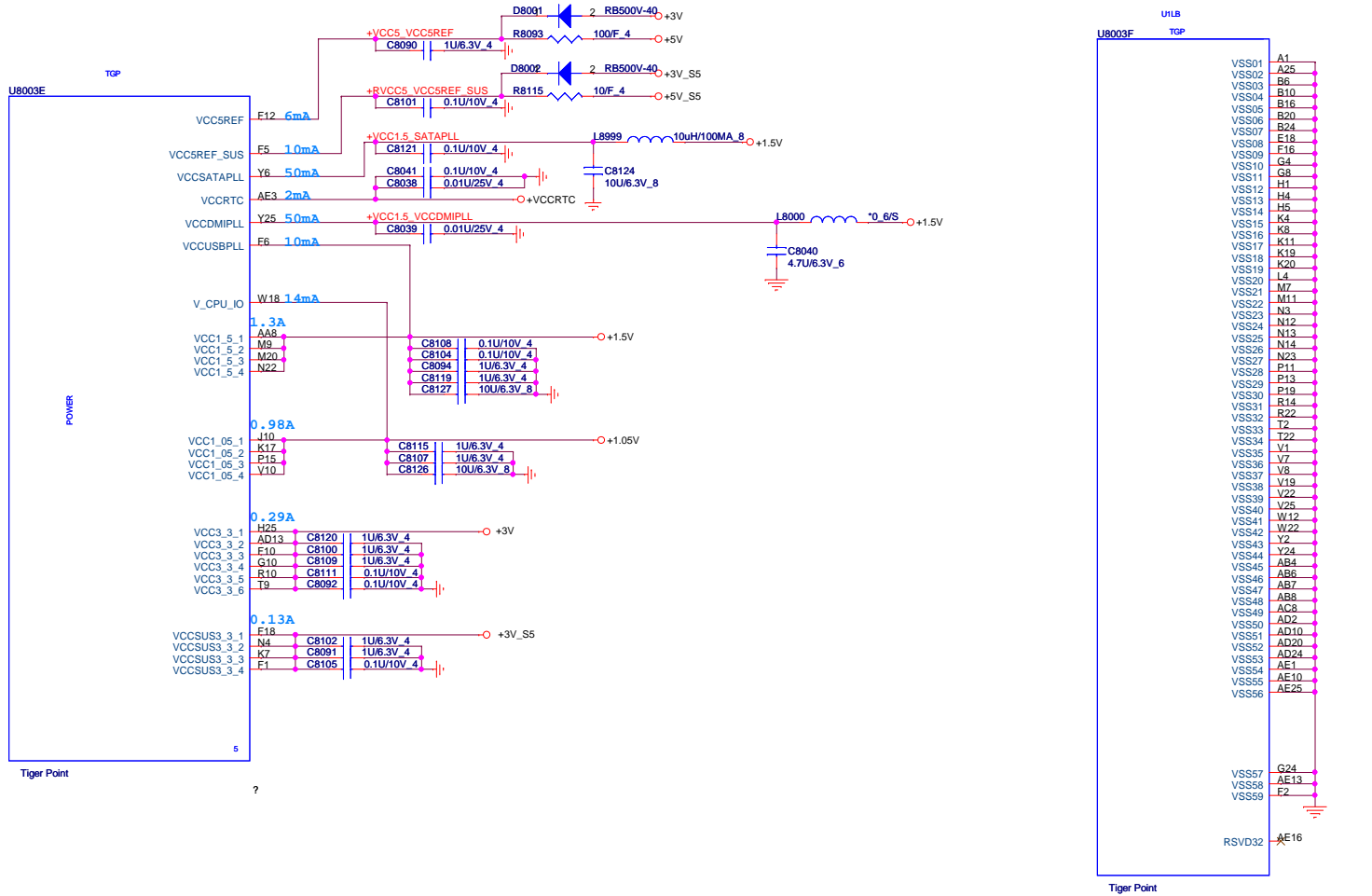
The schematic diagram illustrates the SMBus interface for the ME27N002E. It features two signal lines: SMB0_CLK and SMB0_DATA. Each line is connected to a 3V supply through a 10kF resistor (R8099 for CLK, R8097 for DATA). The signals are then connected to the PCLK_SMB_M and PDAT_SMB_M pins of the ME27N002E device.

ACZ_SDOUT (INT PD)	ACZ_SYNC (INT PD)	Description
0	0	★ 4 x 1s
1	0	Reserved
0	1	Reserved
1	1	1 x 4s(1 port/4 lanes)

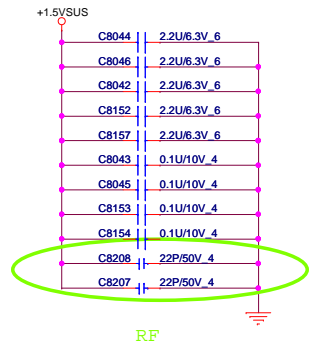


4 3 2

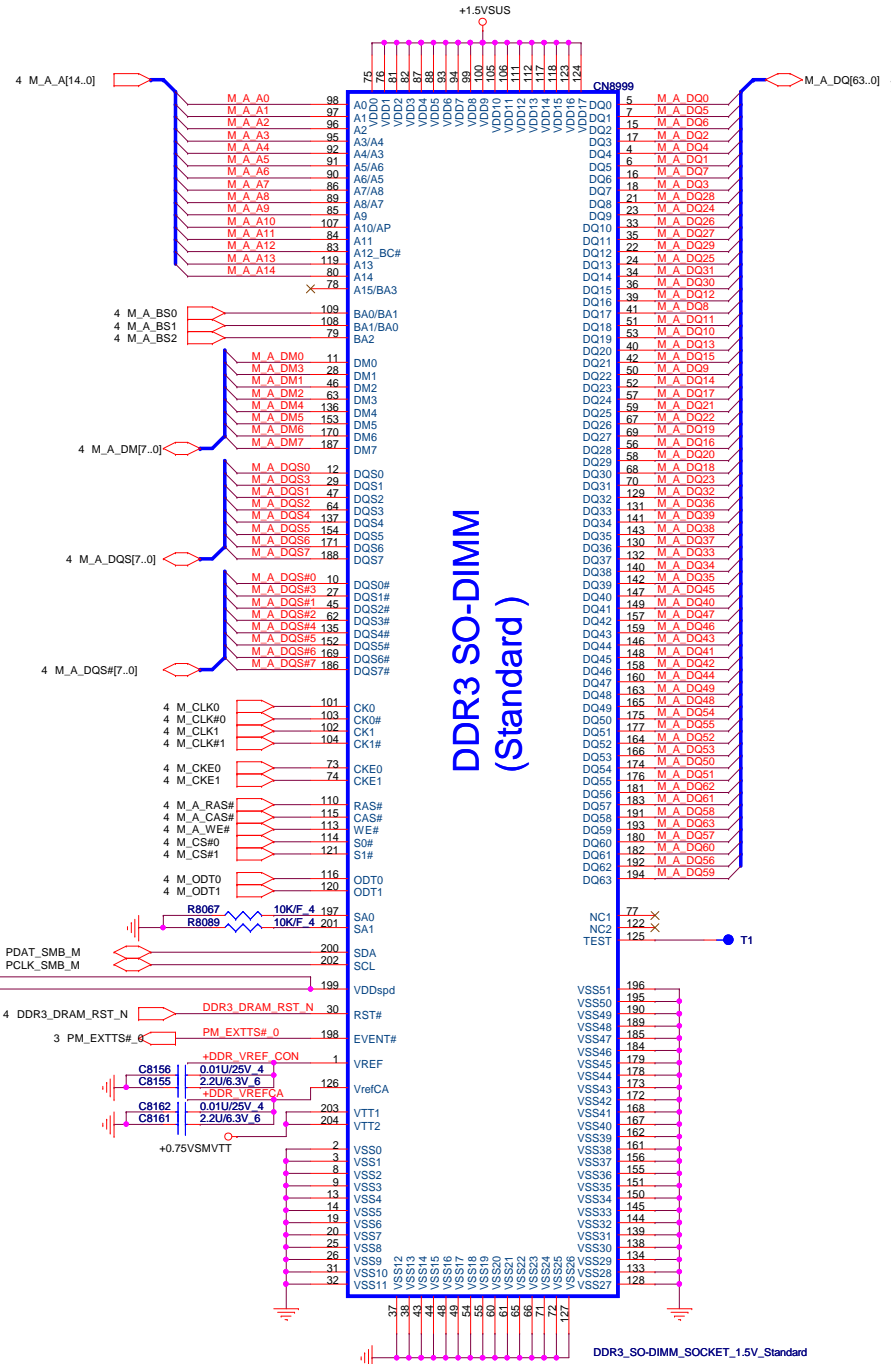
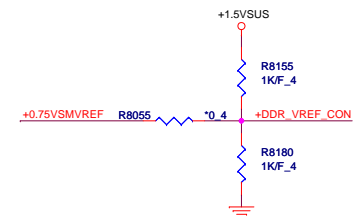
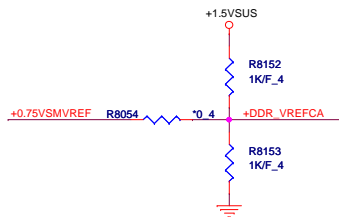
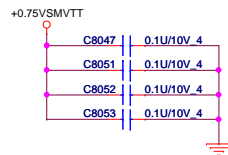
<http://rusefix.com>



+3V 2,3,5,7,8,9,10,12,13,14,15,16,17,18,19,20,21,22,30
 +1.5VSUS 4,5,25,28,29,30
 +0.75VSMVREF 4,25
 +0.75VSMVTT 25



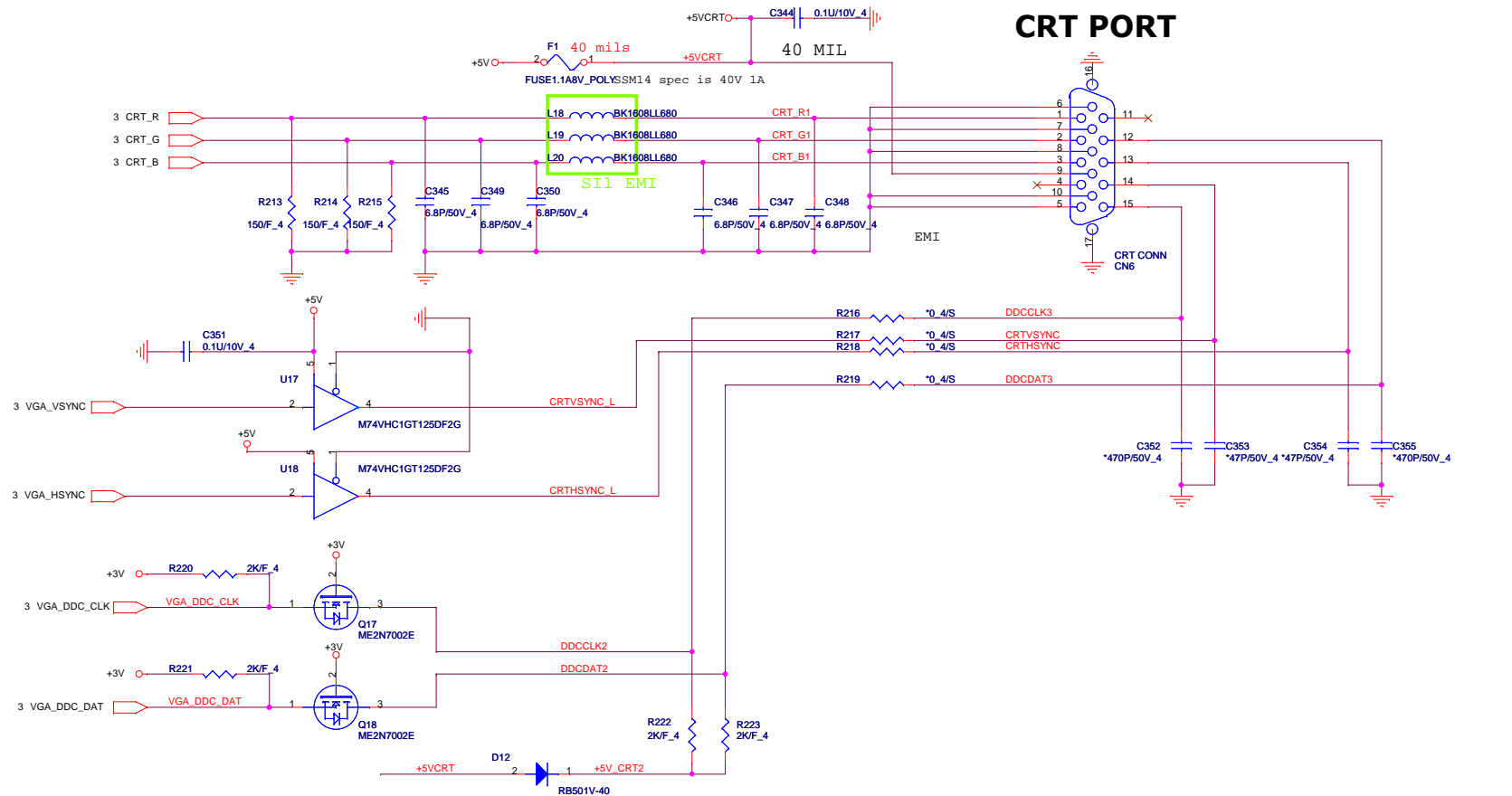
RF



DDR3 SO-DIMM
(Standard)

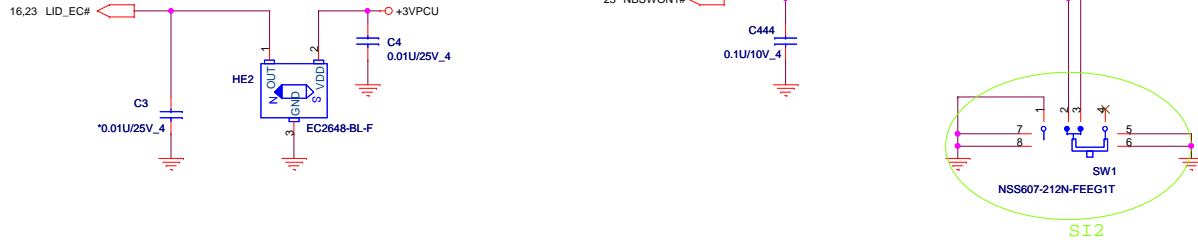
DDR3_SO-DIMM_SOCKET_1.5V_Standard

2,3,5,7,8,9,10,11,13,14,15,16,17,18,19,20,21,22,30 +3V
2,3,5,7,10,14,26,29,31 +1.05V

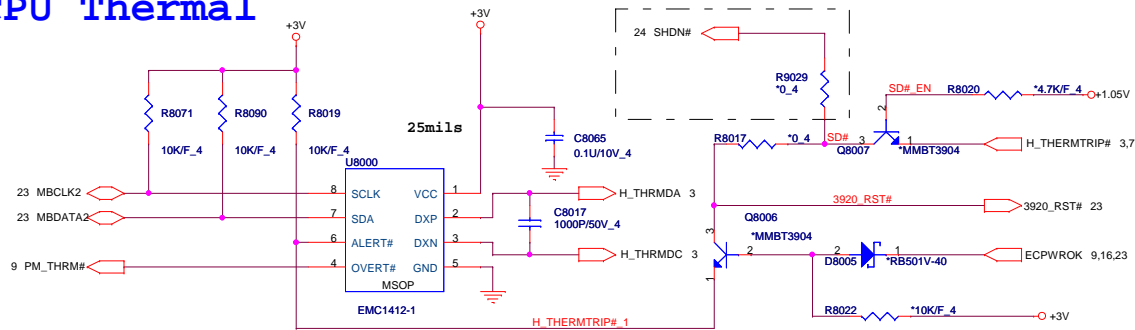




LID Switch



CPU Thermal



1.Level 1 Environment-related Substances Should NEVER be Used.
2.Purchase ink, paint, wire rods, and Molding resins only from the business Partners that Sony approves as Green Partners.

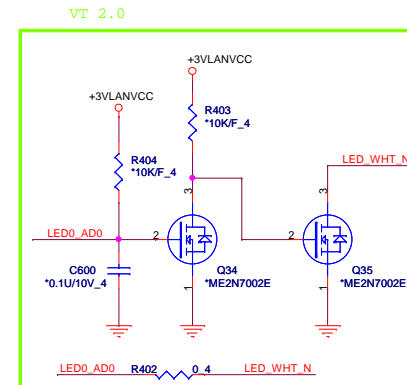
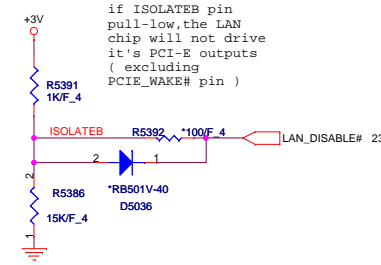
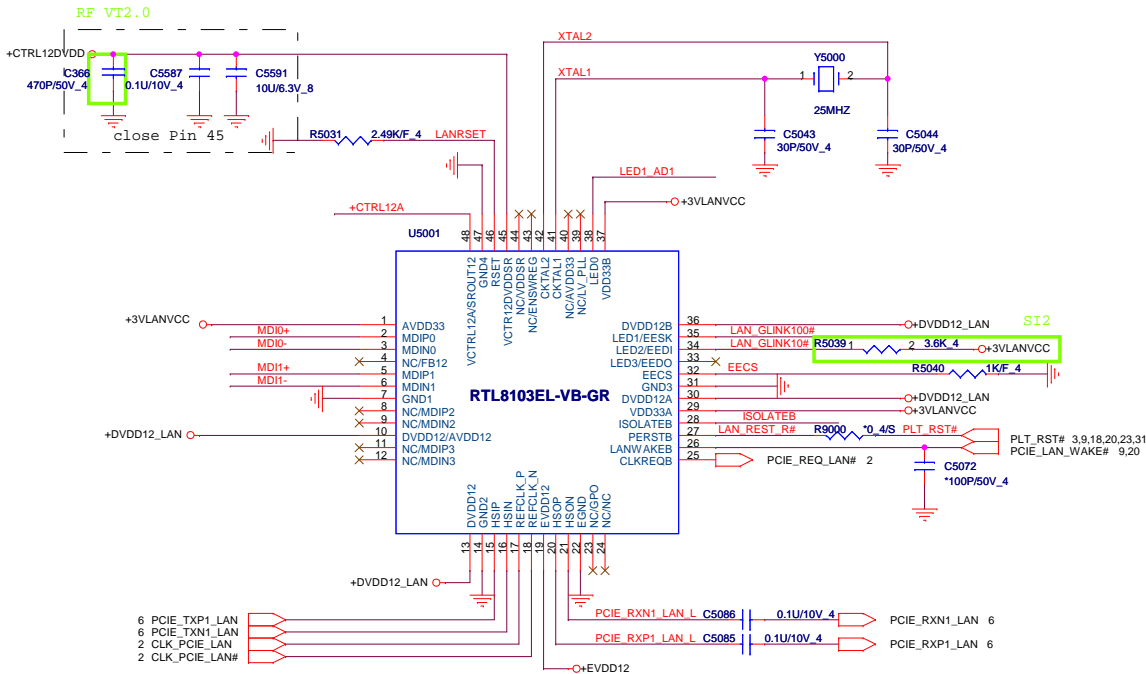
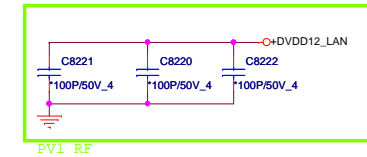
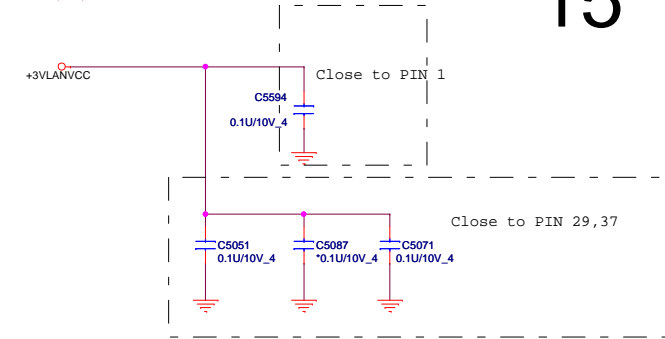
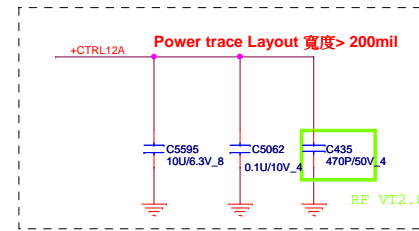
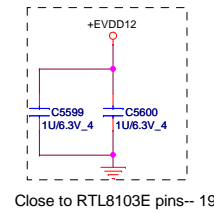


Quanta Computer Inc.

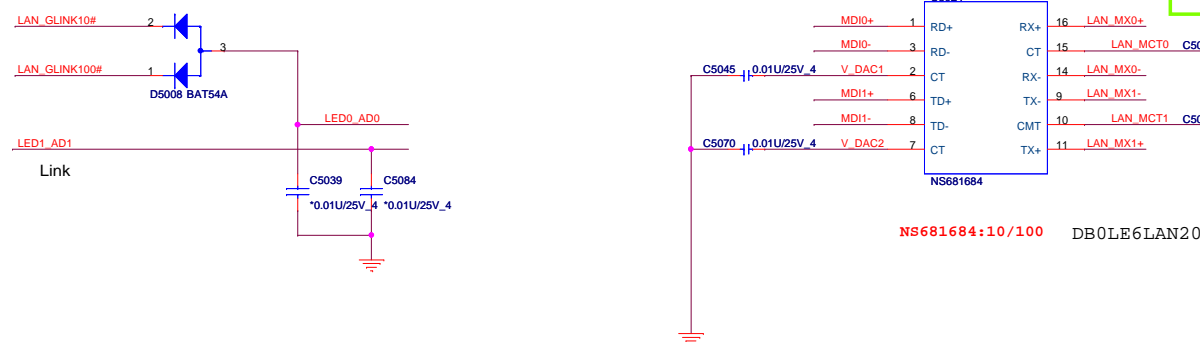
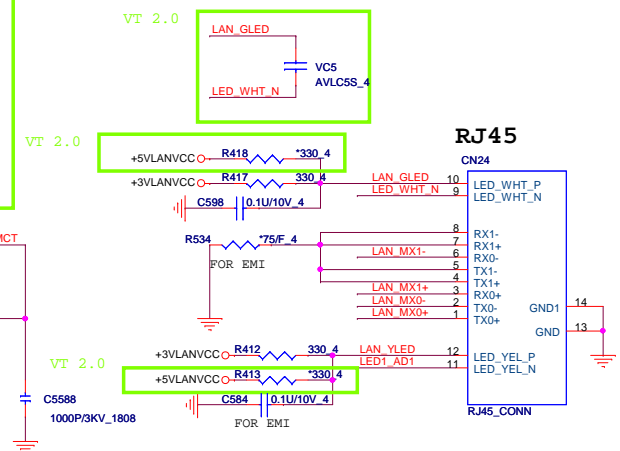
PROJECT : Annika

Size	Document Number	Rev
	Power Bottom	1A

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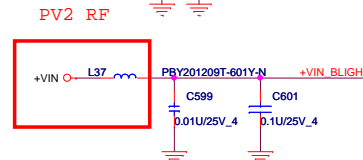
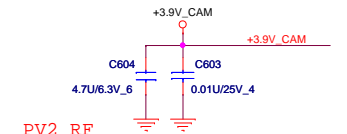
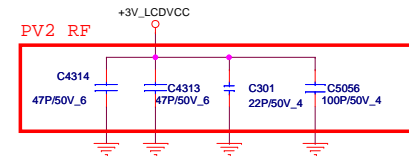
Symbol	Type	Pin No (64-Pin)	Pin No (48-Pin)	Description																									
LED0	O	57	38	<table border="1"> <tr> <td>LED0-0</td><td>00</td><td>01</td><td>10</td><td>11</td></tr> <tr> <td>LED0</td><td>Tx/Rx</td><td>Tx/Rx</td><td>Tx</td><td>Tx</td></tr> <tr> <td>LED1</td><td>LINK100</td><td>LINK</td><td>LINK</td><td>LINK100</td></tr> <tr> <td>LED2</td><td>LINK10</td><td>FULL</td><td>Rx</td><td>LINK10</td></tr> <tr> <td>LED3</td><td>NA</td><td>NA</td><td>NA</td><td>NA</td></tr> </table>	LED0-0	00	01	10	11	LED0	Tx/Rx	Tx/Rx	Tx	Tx	LED1	LINK100	LINK	LINK	LINK100	LED2	LINK10	FULL	Rx	LINK10	LED3	NA	NA	NA	NA
LED0-0	00	01	10	11																									
LED0	Tx/Rx	Tx/Rx	Tx	Tx																									
LED1	LINK100	LINK	LINK	LINK100																									
LED2	LINK10	FULL	Rx	LINK10																									
LED3	NA	NA	NA	NA																									
LED1	O	56	35																										
LED2	O	55	34																										
LED3	O	54	33																										



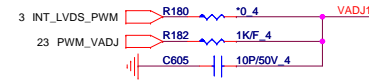
2,3,5,7,8,9,10,11,12,13,14,15,17,18,19,20,21,22,30 +3V
 9,14,20,21,22,23,24,26,27,30 +3VPCU
 10,12,17,19,20,22,30 +5V
 24,25,26,27,29,30 +VIN

LED Panel(LDS)

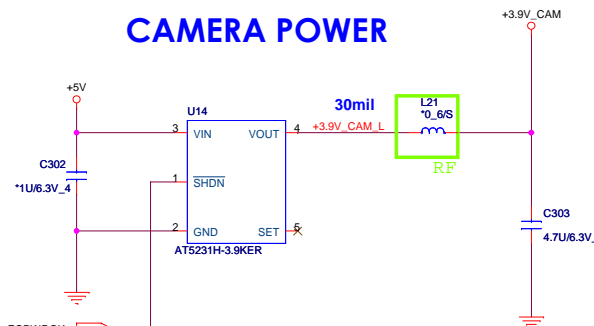
Request by HP RF(47Px2)



Close to LCD Connector

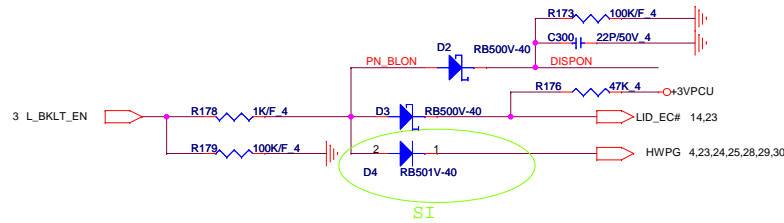


CAMERA POWER

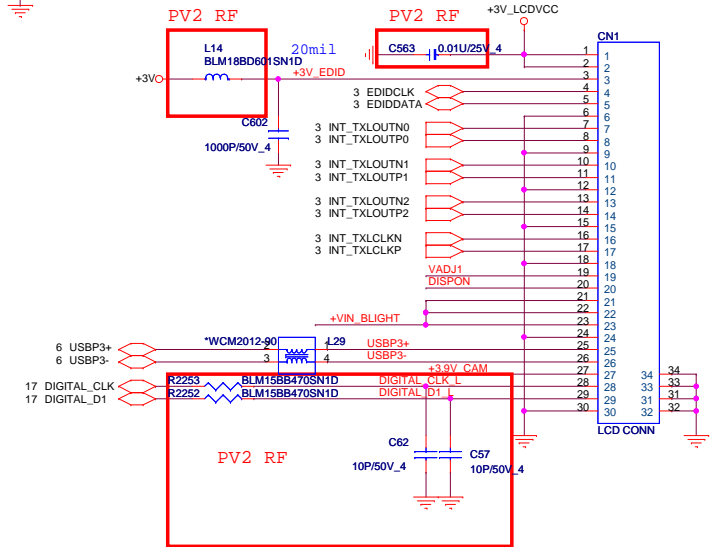
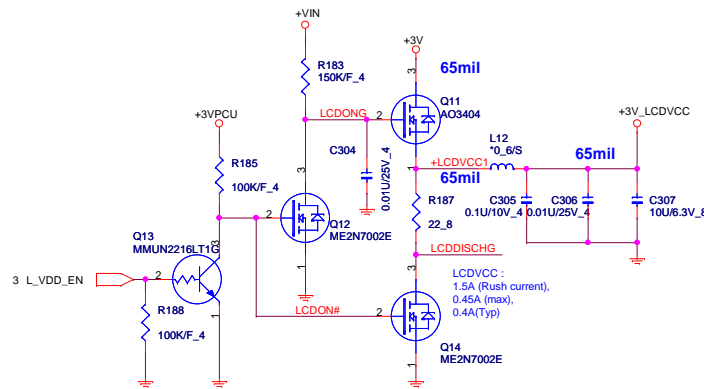


$$V_{out} = 1.25 (1 + R1/R2)$$

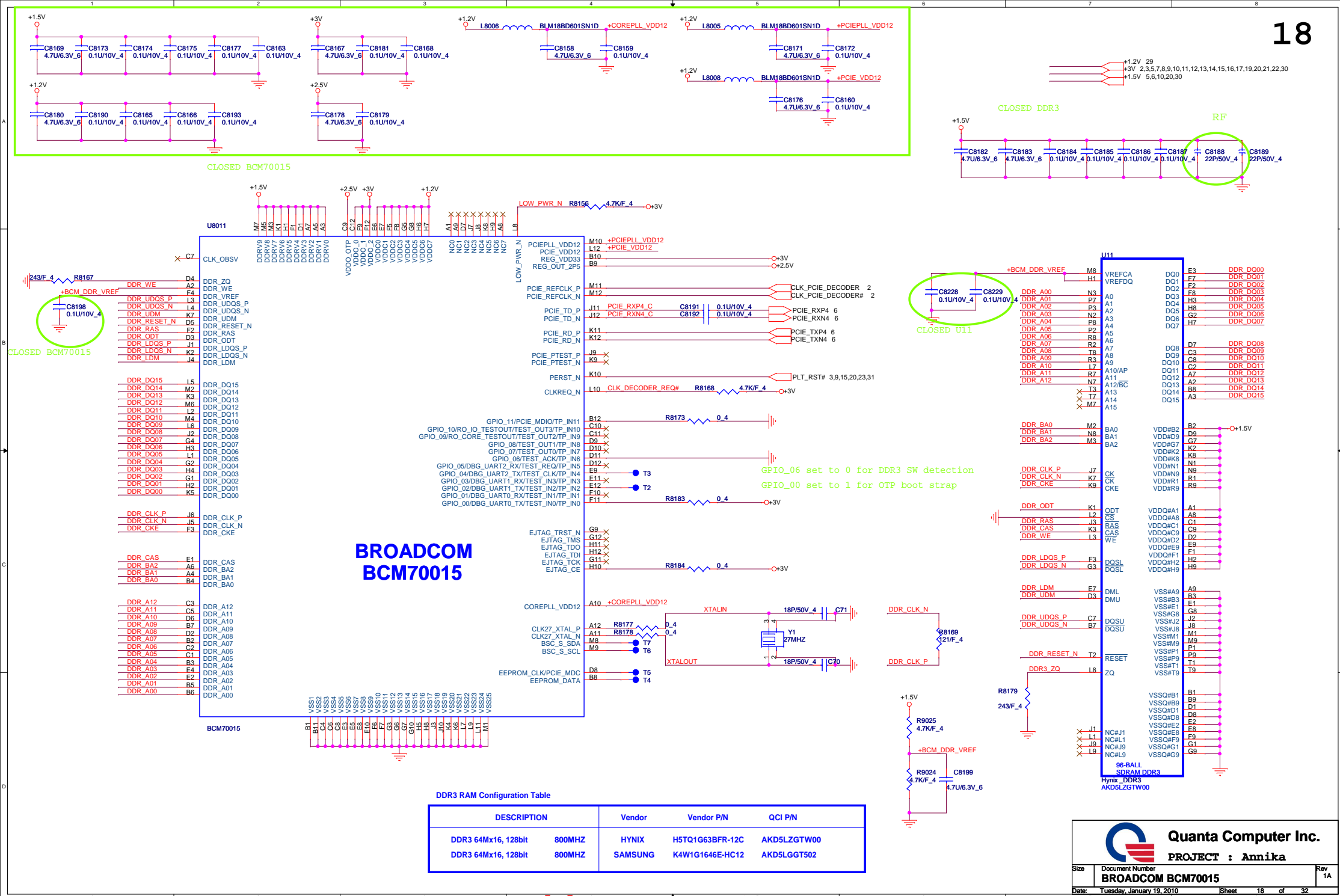
Backlight Control(LDS)



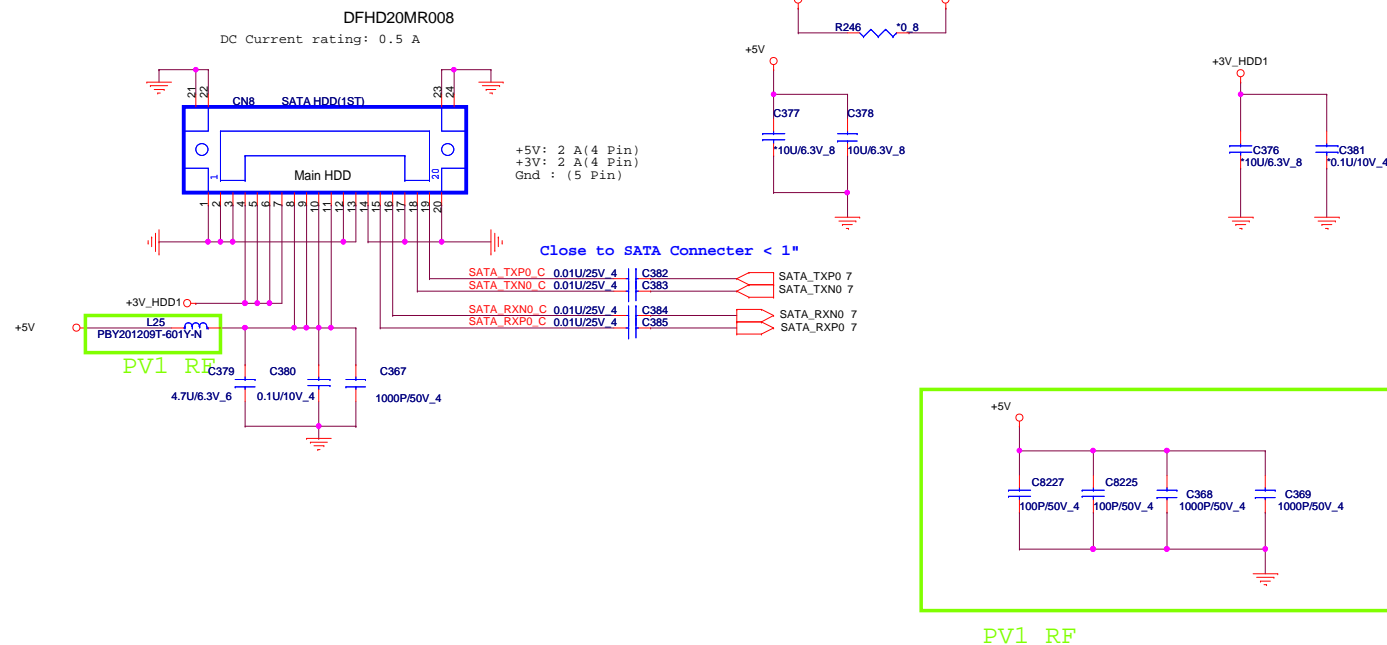
LCD POWER SWITCH



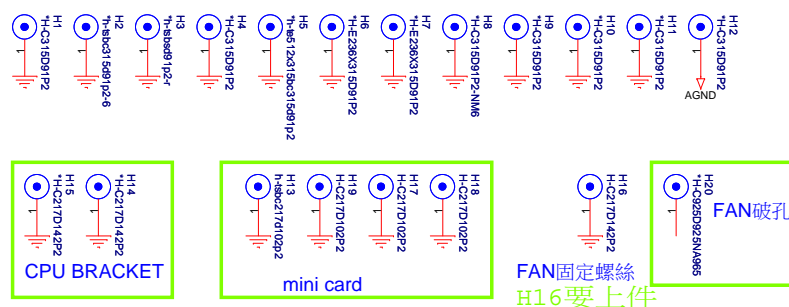




2.5" SATA HDD OR SSD(TOSHIBA)

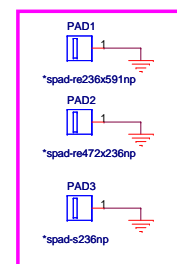


M/B Screw Hole

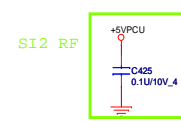
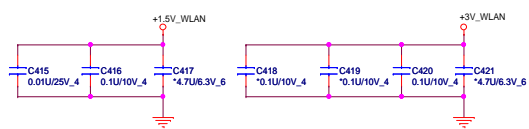
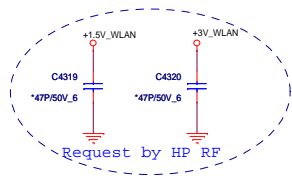


H13/17/18/19 要上件

EMI spring

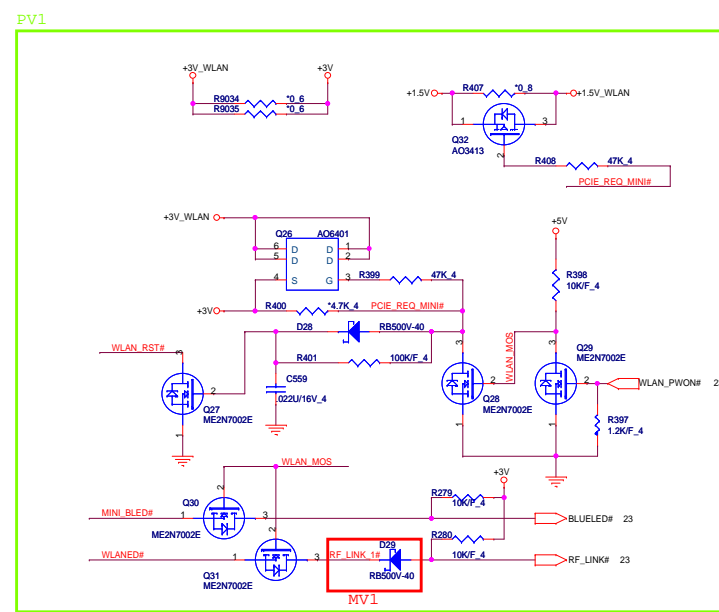
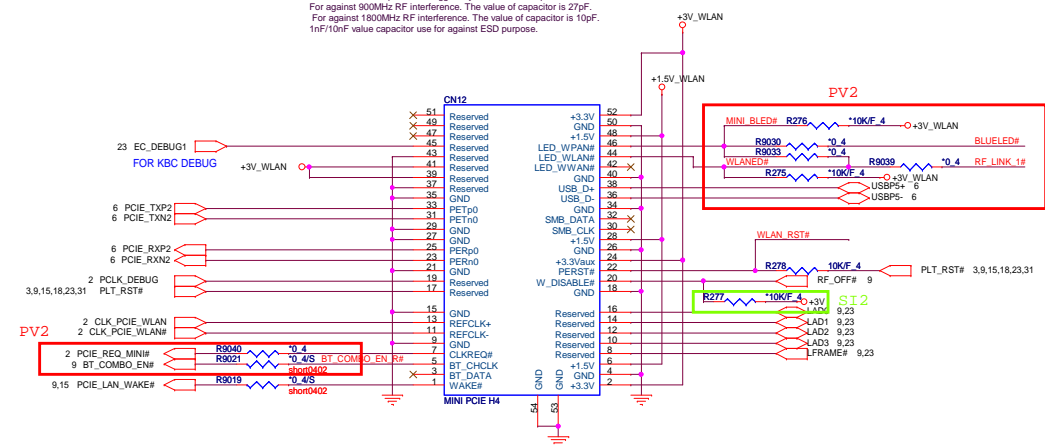


+3VPCU 9,14,16,21,22,23,24,26,27,30
 +1.5V 5,6,10,18,30
 +3V 2,3,5,7,8,9,10,11,12,13,14,15,16,17,18,19,21,22,30
 +5V 10,12,16,17,19,22,30

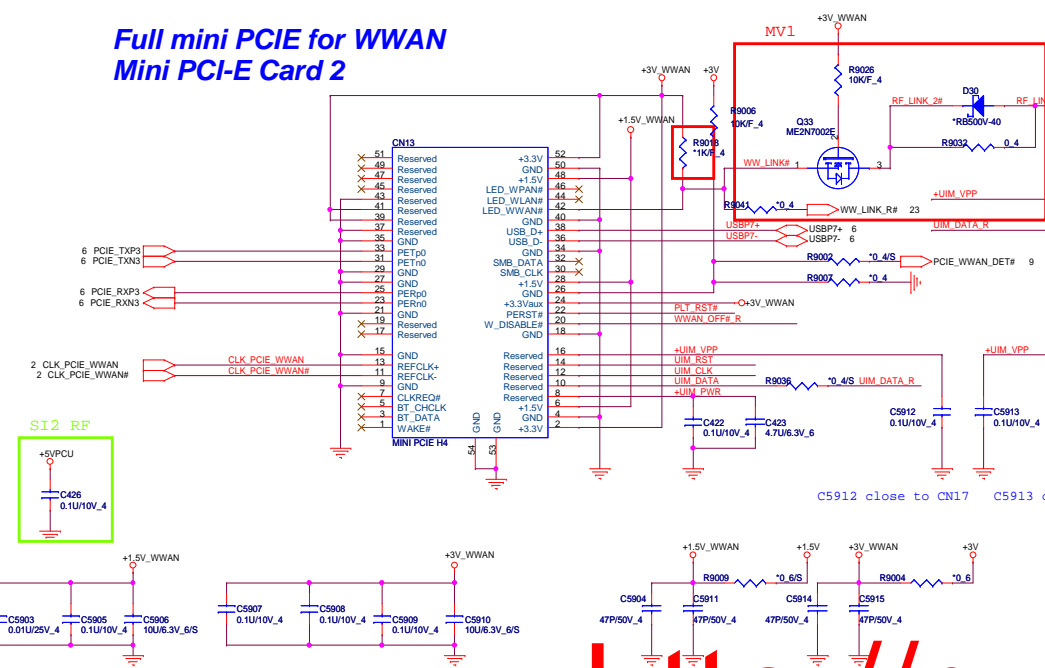


Mini PCI-E Card 1 Half Mini PCI-E WLAN

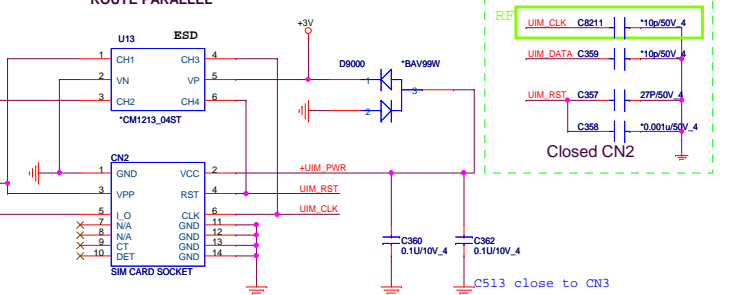
The value of the capacitor is suggest by Siemens HQ expert.
 For against 900MHz RF interference. The value of capacitor is 27pF.
 For against 1800MHz RF interference. The value of capacitor is 10pF.
 1nF/10nF value capacitor use for against ESD purpose.



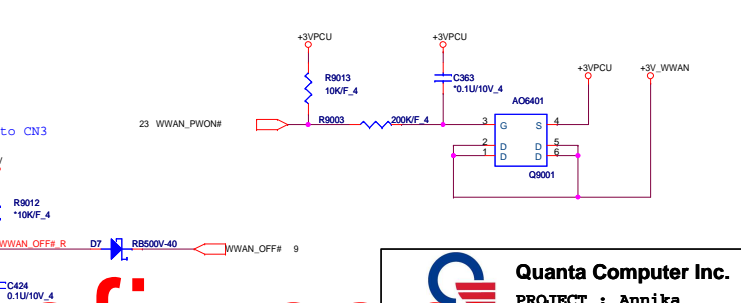
Full mini PCIE for WWAN Mini PCI-E Card 2



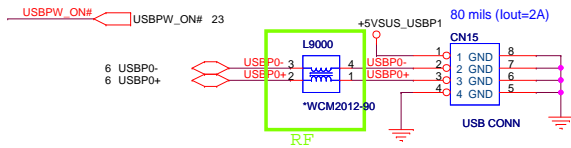
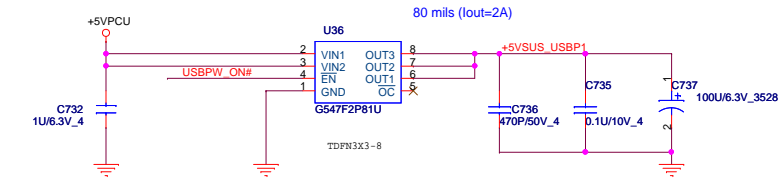
SIM CARD SIGNALS ROUTE PARALLEL



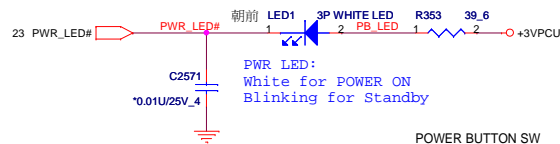
SIM CARD



1x Left side USB port supports Keyed USB.

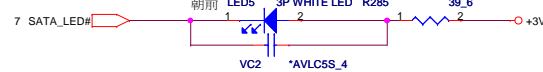


PWR Button/LED

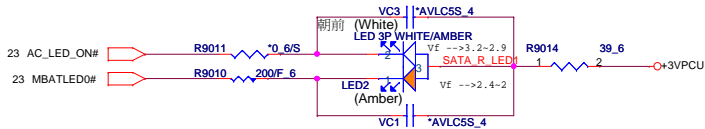


POWER BUTTON SW

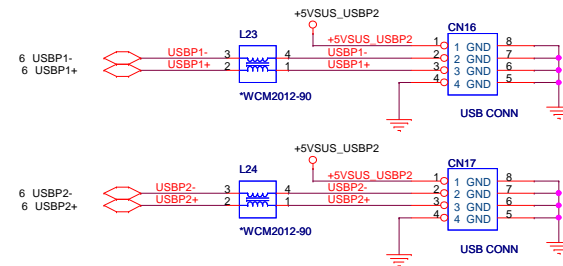
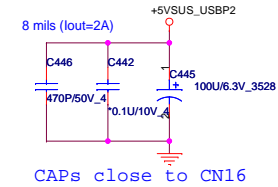
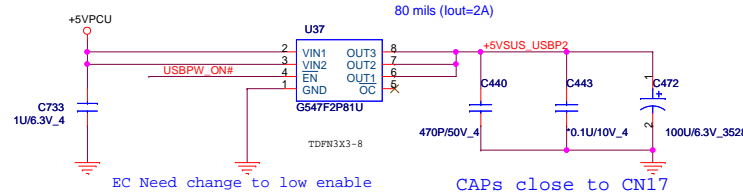
SATA/LED



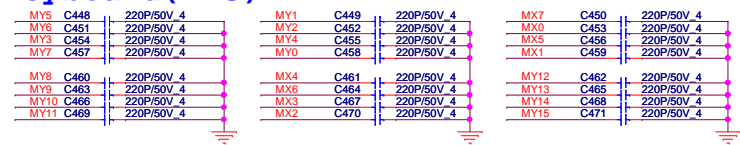
Charging & Discharging/LED



For Right 2xUSB Ports PWR

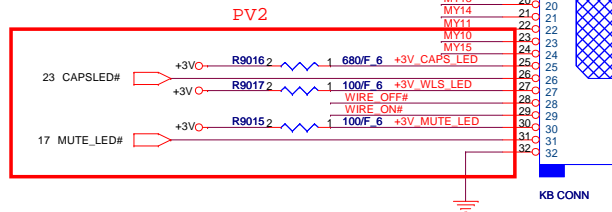
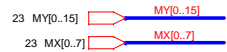
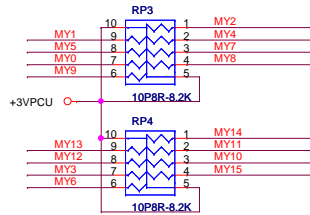


Keyboard (KBC)

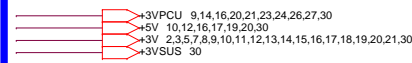
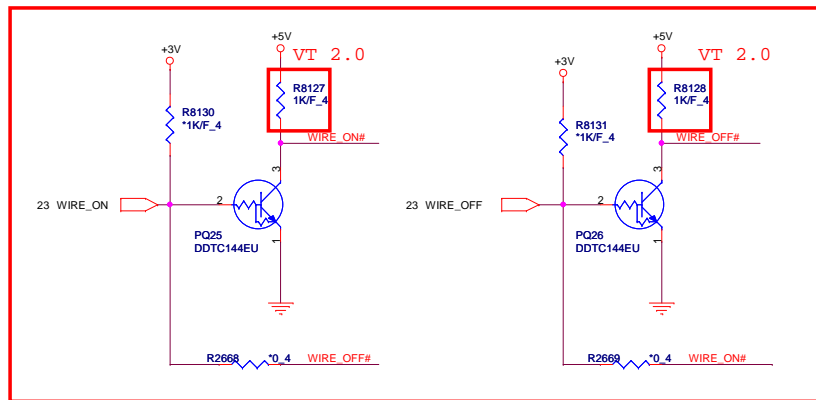


Modify CN2 footprint from bl137-32r1-tand-32p-1 to 196033-32041-32p-1

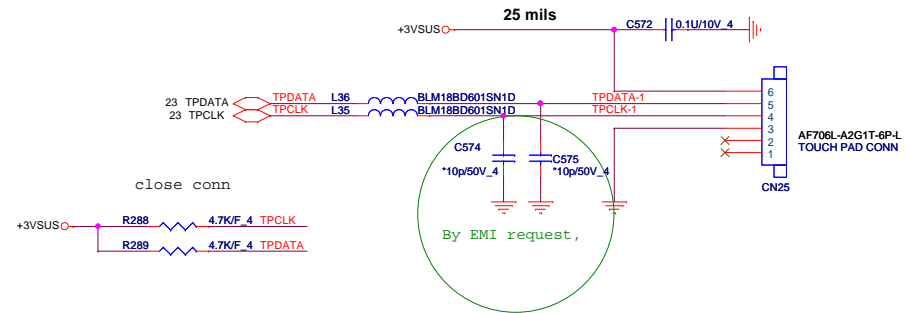
KEYBOARD PULL-UP



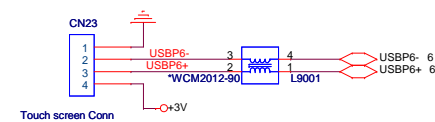
PV2



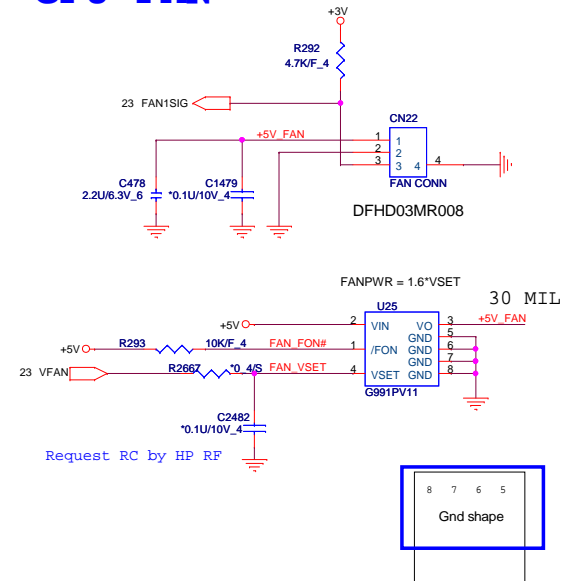
TOUCH PAD CONNECTOR

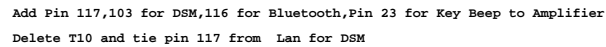


TOUCH SCREEN



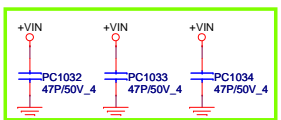
CPU FAN



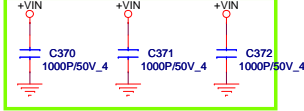


DC/DC +3V_ALW/+5V_ALW/+5V_ALW2 /+15V_ALW

SI2_RF

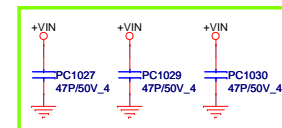


PV1_RF

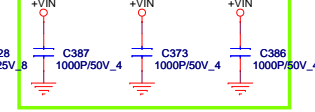


+5VPCU Volt +/- 5%
Countinue current:4A
Peak current:5.5A
OCP minimum 6A

SI2_RF

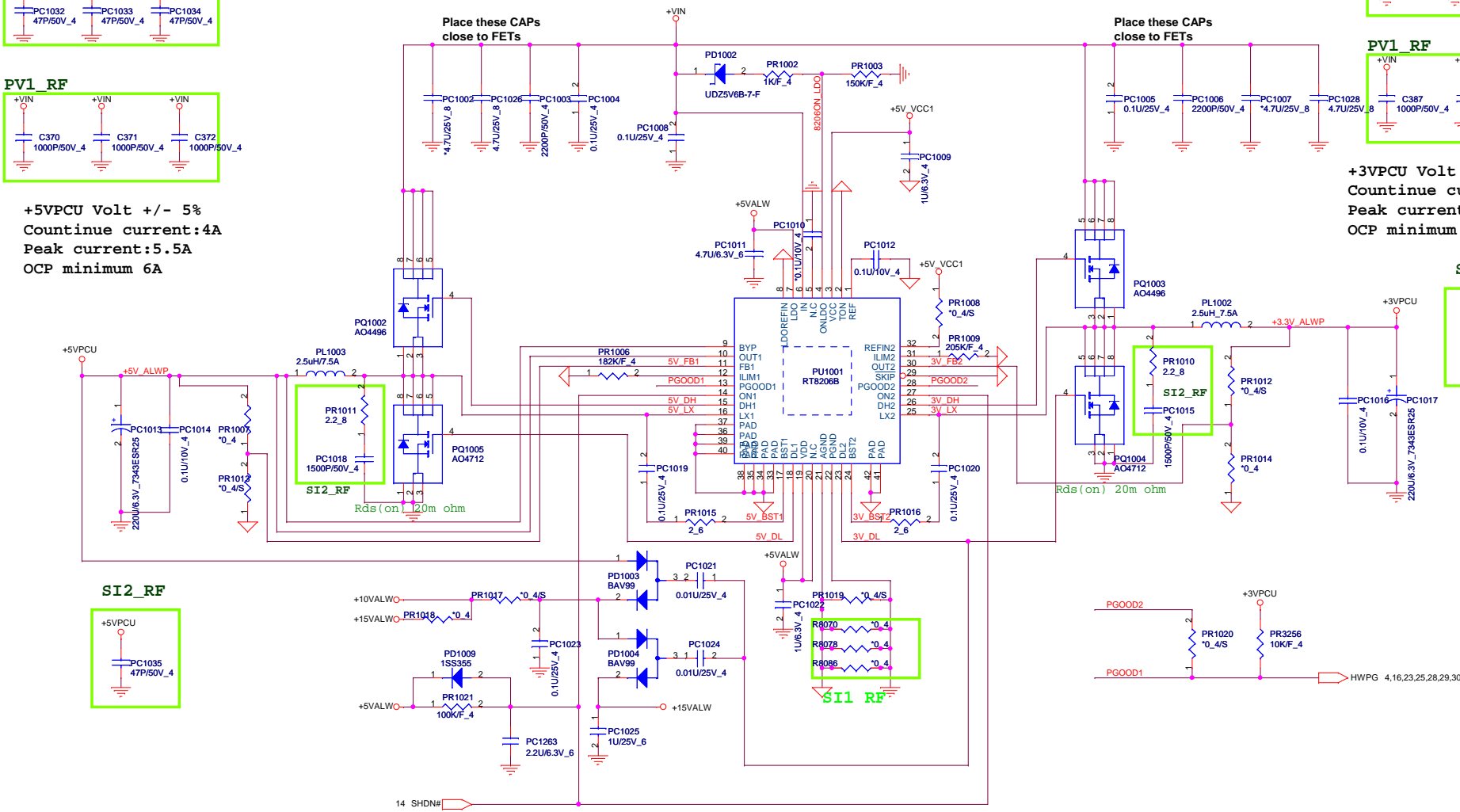
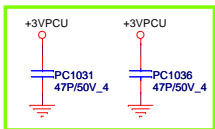


PV1_RF

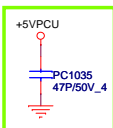


+3VPCU Volt +/- 5%
Countinue current:4A
Peak current:5.5A
OCP minimum 6A

SI2_RF

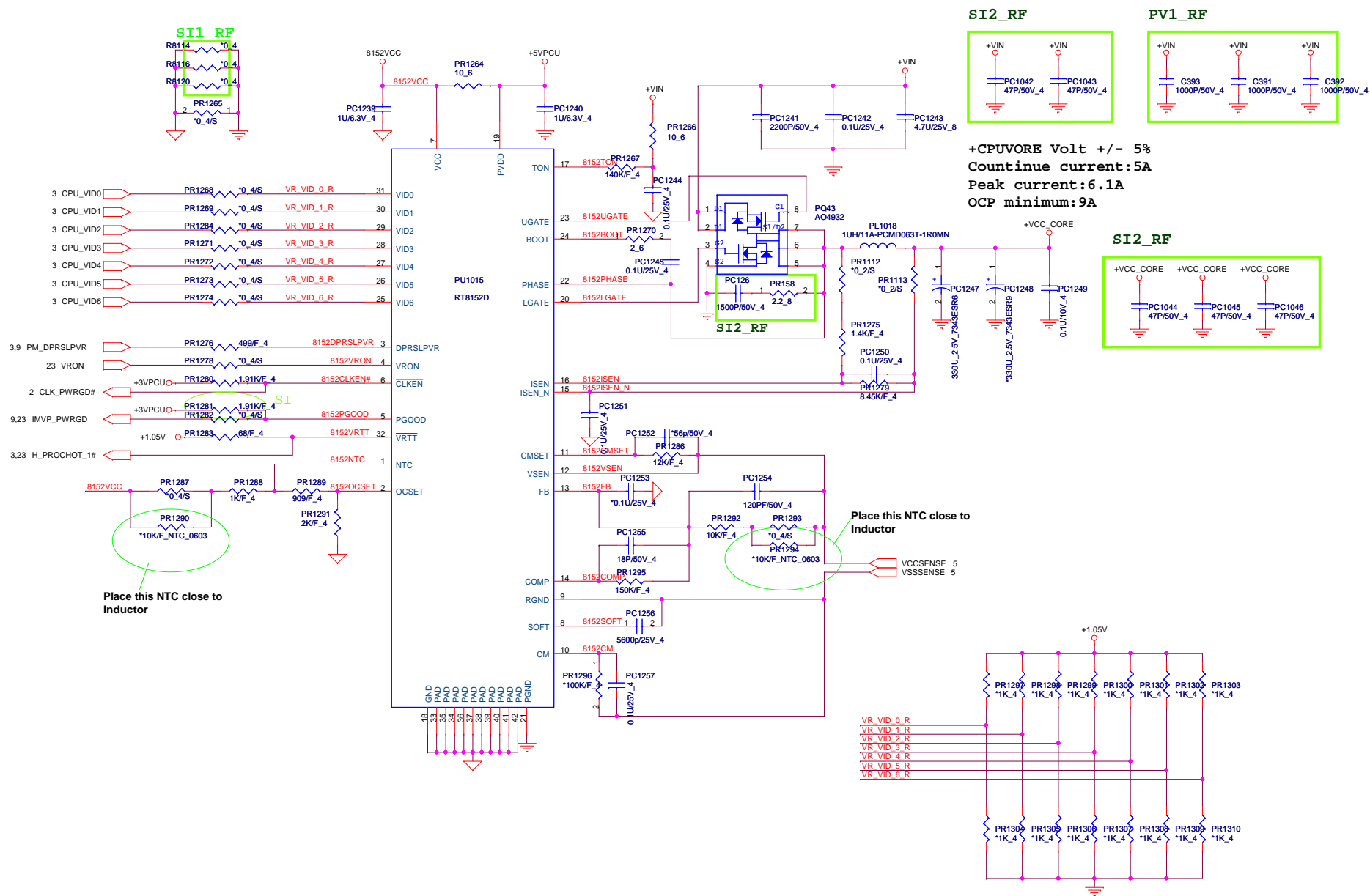


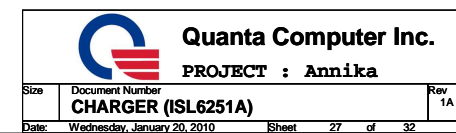
SI2_RF

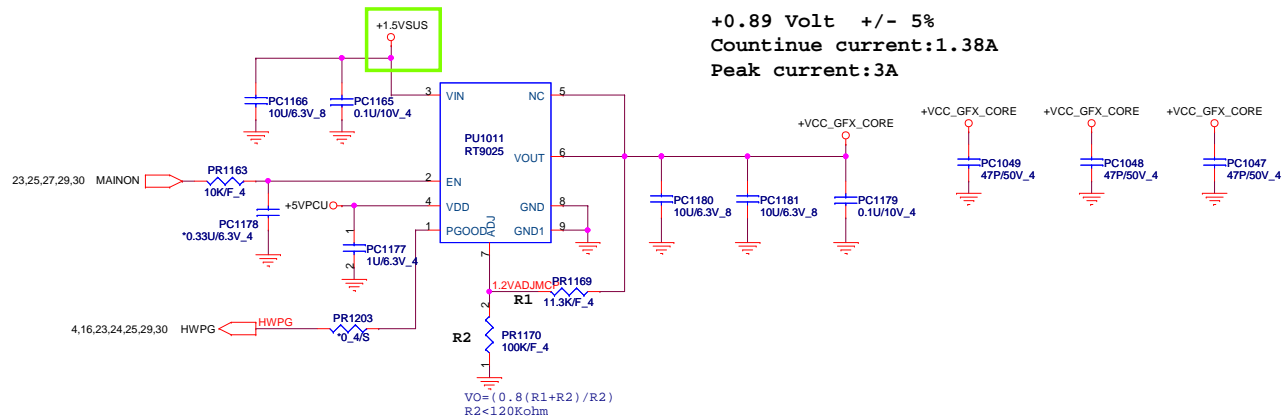


SI1_RF







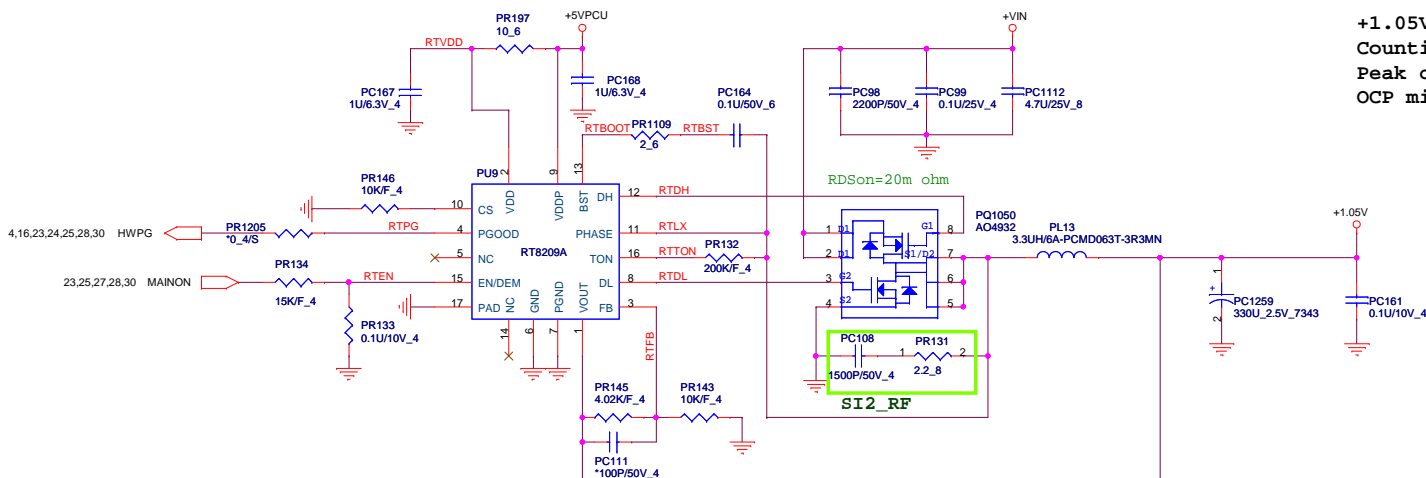


Quanta Computer Inc.

PROJECT : Annika

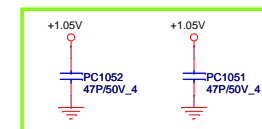
Size	Document Number	Rev
	+1.05V/+1.5V (RT8204)	1A

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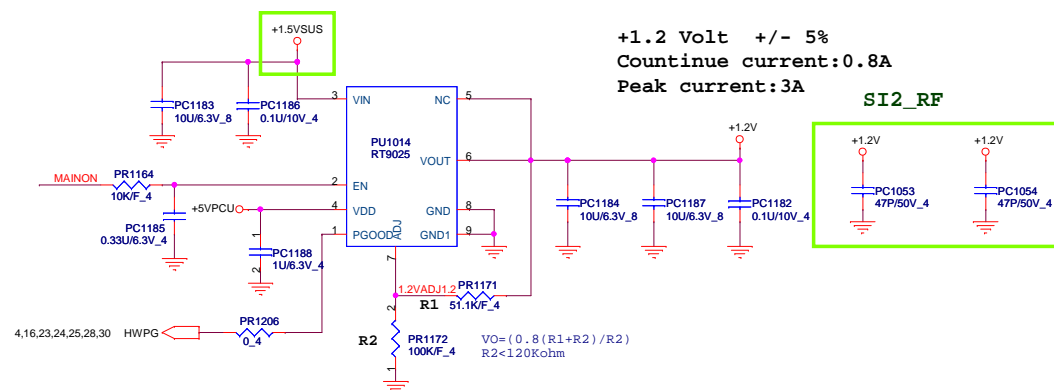
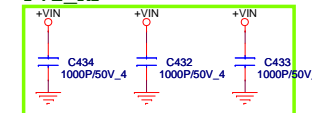


+1.05Volt +/- 5%
Continue current:3A
Peak current:4A
OCP minimum:5A

SI2_RF

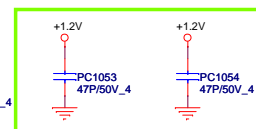


PV1_RF

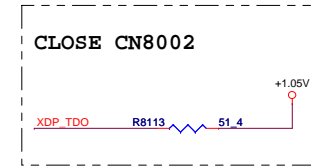
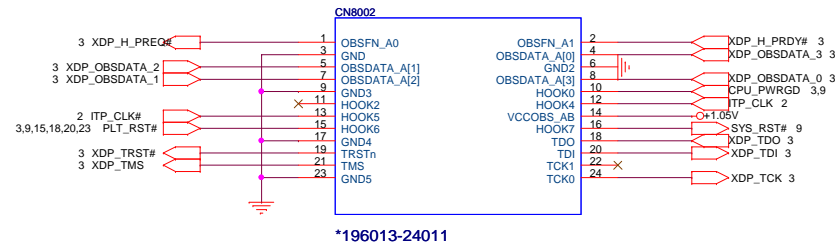


+1.2 Volt +/- 5%
Continue current:0.8A
Peak current:3A

SI2_RF



CPU XDP



Power up sequence

